

ABSTRACT OF THE DISCLOSURE

A gate driving circuit having: a direct current power source; a driving signal source for outputting a high-level or low-level signal; a main switch device,
5 having a gate terminal in which the signal outputted from the driving signal source is inputted, for controlling a conduction state between a source terminal and a drain terminal in correspondence with a level of the signal; a load energized when the
10 conduction state between the source terminal and the drain terminal becomes a conductive state; a reverse current blocking unit, connected between the driving signal source and the gate terminal, to output a signal only in a direction from the driving signal source to
15 the gate terminal; and a regenerative unit, connected between the gate terminal and a high potential side of the direct current power source, which becomes the conductive state when the conduction state between the source terminal and the drain terminal is a non-
20 conductive state. A gate-source threshold voltage to obtain the conductive state between the source terminal and the drain terminal is set higher than an output voltage of the direct current power source.